

● PRINTER RUSH ●

(PTO ASSISTANCE)

Application : <u>09/827648</u>	Examiner : <u>Ngo</u>	GAU : <u>2663</u>
From: <u>NPB</u>	Location: <u>IDC</u> FMF FDC	Date: <u>05/10/05</u>
Tracking #: <u>06093128</u>		Week Date: <u>24/11/05</u>

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449	_____	<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS	_____	<input type="checkbox"/> Foreign Priority
<input type="checkbox"/> CLM	_____	<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW	_____	<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW	_____	<input type="checkbox"/> Other
<input type="checkbox"/> DRW	_____	
<input type="checkbox"/> OATH	_____	
<input type="checkbox"/> 312	_____	
<input checked="" type="checkbox"/> SPEC	_____	

[RUSH] MESSAGE: please provide missing Serial numbers on page 1, lines 9,
11, 13 and 15 (docket No. is listed without serial numbers)

Thank you

[XRUSH] RESPONSE: accepted

see Attachment

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 REV 10/04

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Printed Name: Bobby Slaton
Signature: Bobby Slaton

ATM PROCESSOR FOR SWITCHING IN AN ATM SYSTEM

CROSS REFERENCE TO RELATED APPLICATIONS

Cross reference is made to commonly assigned U.S. Patent Application
No. 09/827660
~~Attorney's Docket No. TI-32578~~ entitled "AAL2 Receiver For Filtering
Signaling/Management Packets In An ATM System", U.S. Patent Application
No. 09/827816
~~Attorney's Docket No. TI-32579~~ entitled "AAL2 Transmitter For Voice-Packed
and Signaling Management-Packed Interleaving On An ATM Connection", and
U.S. Patent Application, *No. 09/827829*
~~Attorney's Docket No. TI-32581~~ entitled "ATM System
Architecture For The Convergence of Data, Voice and Video", U.S. Patent
No. 09/827808
Application ~~Attorney's Docket No. TI-32582~~ entitled "Cell Buffering System
With Priority Cache In An ATM System", the teaching of each of these
applications being incorporated herein by reference and filed herewith.